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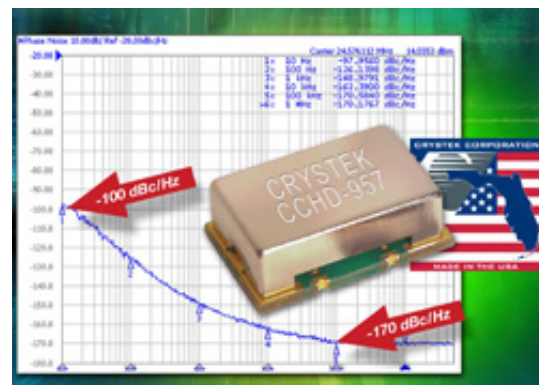
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## Low Phase Noise Clock Oscillator Offers High-performance Frequency Control for HD Audio

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Friday, January 6, 2012

Crystek Corporation launched the CCHD-957, a new HCMOS clock oscillator with standby mode, featuring a low close-in phase noise of -100 dBc/Hz at 10 Hz offset and a typical noise floor of -170 dBc/Hz at 100 kHz offset. This performance makes the HCMOS clock oscillator appropriate for use in applications such as: DACs ([digital](#)-to-analog converters), ADCs (analog-to-digital converters), DAB (digital audio broadcasting), and professional CD audio equipment. The Crystek CCHD-957 HCMOS Clock Oscillator also features a “Standby Function” – when placed in disable mode, the internal oscillator is completely shut down and its output buffer is placed in Tri-State. This family is housed in a 9-mm x 14-mm SMT package and operates with a +3.3-V power supply consuming 15 mA of current. Stability is rated at 20 ppm to 50ppm (0°C to +70°C) and ±25 ppm to 50 ppm (-40°C to +85°C). The CCHD-957 generates frequencies between 10 MHz and 50 MHz. Its output driver is capable of driving ±24 mA, translating to a rise/fall time of ~3 nsec Max at 20 percent to 80 percent Vcc with a 15-pF load.



**Crystek Corporation**

800-237-3061, [www.crystek.com](http://www.crystek.com)