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New Products

Ultra-low phase noise clock oscillator provides high-performance frequency control for HD audio equipment

January 06, 2012 | Paul Buckley | 222902875



Crystek Corporation has launched the CCHD-957, a new Ultra-Low Phase Noise HCMOS Clock Oscillator with Standby Mode, featuring a low close-in phase noise of -100 dBc/Hz @ 10 Hz offset and a typical noise floor of -170 dBc/Hz @ 100kHz offset. The performance makes Crystek's HCMOS Clock Oscillator family an ideal choice for use in applications such as: DACs (digital-to-analog converters), ADCs (analog-to-digital converters), DAB (digital audio broadcasting), and professional CD audio equipment.

The Crystek CCHD-957 HCMOS Clock Oscillator also features a 'Standby Function' – when placed in disable mode, the internal oscillator is completely shut down and its output buffer is placed in Tri-State. This family is housed in a 9x14 mm SMT package and operates with a +3.3 V power supply consuming 15 mA of current. Stability is rated at 20-50 ppm (0 to +70°C) and ±25-50 ppm (-40 to +85°C).

The CCHD-957 generates frequencies between 10 MHz and 50 MHz. Its output driver is capable of driving ±24 mA, translating to a rise/fall time of ~3 nsec Max at 20% to 80% Vcc with a 15 pF load.

The Crystek CCHD-957 HCMOS Clock Oscillator is made in the USA and available from Crystek's stocking distributors.

Visit Crystek at www.crystek.com

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