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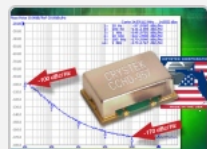
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New Products

Clock oscillator has a phase noise of -100 dBc/Hz at 10Hz offset for audio equipment

January 06, 2012 | Julien Happich | 222910692



Crystek has launched the CCHD-957, a new ultra-low phase noise HCMOS clock oscillator with standby mode, featuring an extremely low close-in phase noise of -100 dBc/Hz at 10Hz offset and a typical noise floor of -170 dBc/Hz at 100kHz offset. This performance makes Crystek's HCMOS Clock Oscillator family suitable for use in applications such as DACs, ADCs, digital audio broadcasting and professional CD audio equipment.

When placed in disable mode, the CCHD-957's internal oscillator is completely shut down and its output buffer is placed in tri-state. The device is housed in a 9x14mm SMT package and operates with a +3.3V power supply consuming 15mA of current. Stability is rated at 20-50ppm (0°C to +70°C) and ±25-50ppm (-40°C to +85°C). The CCHD-957 generates frequencies between 10 and 50MHz. Its output driver is capable of driving ±24mA, translating to a rise/fall time of about 3ns maximum at 20% to 80% Vcc with a 15pF load.

Visit Crystek at www.crystek.com

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