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Ultra-low phase noise clock oscillator from Crystek

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Crystek Corporation has launched the CCHD-957, a new Ultra-Low Phase Noise HCMOS Clock Oscillator with Standby Mode, featuring an extremely low close-in phase noise of -100 dBc/Hz @ 10Hz offset and a typical noise floor of -170 dBc/Hz @ 100kHz offset. This performance makes Crystek's HCMOS Clock Oscillator family an industry-leading choice for use in applications such as: DACs (digital-to-analog converters), ADCs (analog-to-digital converters), DAB (digital audio broadcasting), and professional CD audio equipment.

The Crystek CCHD-957 HCMOS Clock Oscillator also features a "Standby Function" - when placed in disable mode, the internal oscillator is completely shut down and its output buffer is placed in Tri-State. This family is housed in a 9x14 mm SMT package and operates with a +3.3V power supply consuming 15mA of current. Stability is rated at 20-50ppm (0°C to +70°C) and ±25-50ppm (-40°C to +85°C).

The CCHD-957 generates frequencies between 10MHz and 50MHz. Its output driver is capable of driving ±24mA, translating to a rise/fall time of ~3nsec Max at 20% to 80% Vcc with a 15pF load.

The Crystek CCHD-957 HCMOS Clock Oscillator is made in the U.S.A. and available from Crystek's stocking distributors. For pricing details, contact Crystek Corporation.

Family datasheets are available for download at www.crystek.com.

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