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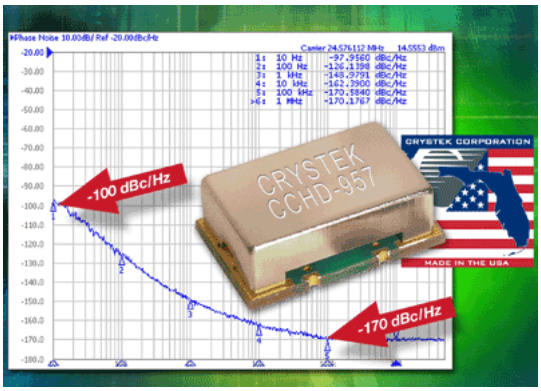
Clock oscillator for HD audio equipment

Suitable for HD audio applications, the CCHD-957 HCMOS clock oscillator with standby mode, features a close-in phase noise of 100 dBc/Hz at 10-Hz offset and a typical noise floor of 170 dBc/Hz at 100kHz offset. Additional features include a frequency range from 10 to 50 MHz, and input voltage of 3.3±0.3 V, and an input current of 15 mA typical and 1.5 mA maximum in disable mode.



Disable time is 200 ns maximum and start-up time is 1 ms typical. The part is housed in a 9 x 14 mm SMT package and has a temperature range from 0 to 70C. (Contact company for pricing available now)

By Christina Nickolas

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