



TECHNOLOGY / ASSETS Time Frequency

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Philippe Corvisier , electronics, 09/01/2012 at 15:19



The clock circuit of Crystek CCHD-957 is a 3.3 V HCMOS oscillator with low phase noise in the proposed SMD 9 x 14 mm. This circuit generates a fixed frequency in the range 10 MHz to 50 MHz with a stability of ± 25 ppm or ± 50 ppm from -40 ° C to +85 ° C. It has a sleep mode for reduced consumption.

The newcomer shows a phase noise of -100 dBc / Hz at 10 Hz of the center frequency, for a noise floor located -170 dBc / Hz at 100 kHz. This performance predestine the CCHD-957 systems data conversion (ADC and DAC), DAB applications and professional audio equipment.

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