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New Products

Low phase noise clock oscillator targets HD audio

January 16, 2012 | Jean-Pierre Joosting | 222902437



The CCHD-957 from Crystek is an ultra-low phase noise HCMOS clock oscillator with standby mode — close-in phase noise comes in at -100 dBc/Hz at 10Hz offset and the oscillator features a typical noise floor of -170 dBc/Hz at 100kHz offset.

This performance makes this HCMOS clock oscillator family an industry-leading choice for use in applications such as DACs (digital-to-analog converters), ADCs (analog-to-digital converters), DAB (digital audio broadcasting), and professional CD audio equipment.

The CCHD-957 HCMOS clock oscillator also features a "Standby Function" — when placed in disable mode, the internal oscillator is completely shut down and its output buffer is placed in Tri-State. This family is housed in a 9- x 14-mm SMT package and operates with a +3.3 V power supply consuming 15 mA of current. Stability is rated at 20- to 50-ppm (0 °C to +70 °C) and ±25- to 50-ppm (-40 °C to +85 °C).

The CCHD-957 generates frequencies between 10 MHz and 50 MHz. Its output driver is capable of driving ±24 mA, translating to a rise/fall time of around 3 ns maximum at 20% to 80% Vcc with a 15 pF load.

For further information: www.crystek.com.

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LTE will ramp up to dominate by end 2010

3G with HSPA will be preferred

WiMAX will get a second life in emerging markets

None, it will be a mix of these technologies

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