



Crystek launches ultra-low phase noise clock oscillator

By [Crystek](#)

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Crystek Corp. has launched the CCHD-957, a new ultra-low phase noise HCMOS clock oscillator with standby mode, featuring an extremely low close-in phase noise of -100 dBc/Hz at 10 Hz offset and a typical noise floor of -170 dBc/Hz at 100 kHz offset. This performance makes Crystek's HCMOS clock oscillator family a good choice for use in applications such as digital-to-analog converters (DAC), analog-to-digital converters (ADC), digital audio broadcasting (DAB) and professional CD audio equipment.

The Crystek CCHD-957 HCMOS clock oscillator also features a "standby function" – when placed in disable mode, the internal oscillator is completely shut down and its output buffer is placed in tri-state. This family is housed in a 9 x 14 mm SMT package and operates with a +3.3 V power supply consuming 15 mA of current. Stability is rated at 20 to 50ppm (0° to +70°C) and ±25-50 ppm (-40° to +85°C).

The CCHD-957 generates frequencies between 10 and 50 MHz. Its output driver is capable of driving ±24 mA, translating to a rise/fall time of ~3 nsec max at 20 to 80 percent Vcc with a 15 pF load.