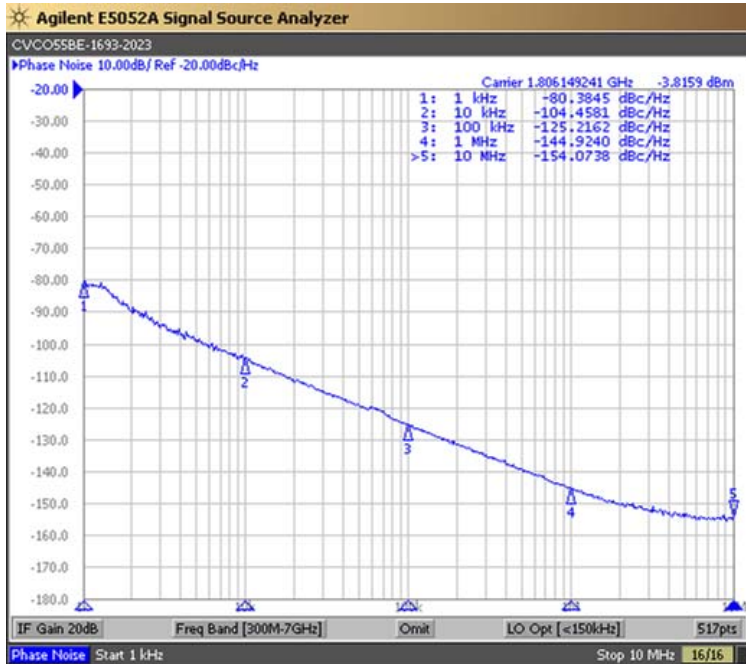
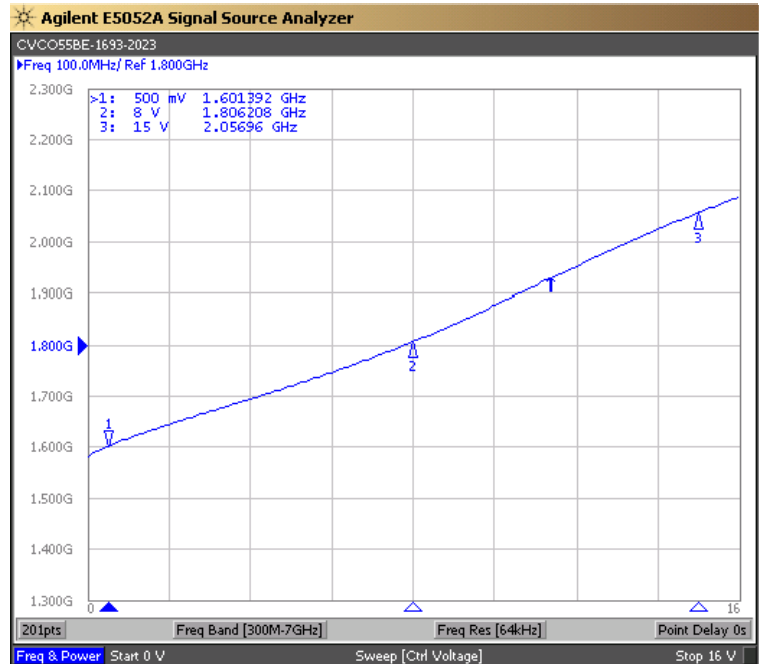


PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			1693	MHz
Upper Frequency:	2023			MHz
Tuning Voltage:	0.5		15.0	VDC
Supply Voltage:	7.6	8.0	8.4	VDC
Output Power:	-6.0	-4.0	-2.0	dBm
Supply Current:		15	25	mA
Harmonic Suppression (2 nd Harmonic):		-15	-10	dBc
Pushing:		0.5	1.0	MHz/V
Pulling, all Phases:		1.0	2.0	MHz pk-pk
Tuning Sensitivity:		25		MHz/V
Phase Noise @ 10kHz offset:		-98	-95	dBc/Hz
Phase Noise @ 100kHz offset:		-118	-115	dBc/Hz
Load Impedance:		50		Ω
Input Capacitance:			15	pF
Operating Temperature Range:	-40		+85	$^{\circ}\text{C}$
Storage Temperature Range:	-45		+90	$^{\circ}\text{C}$

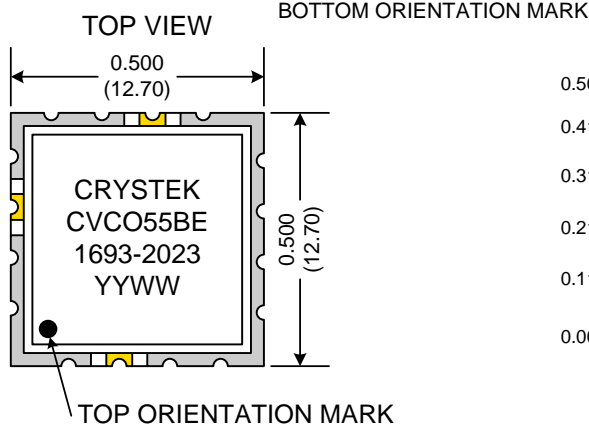
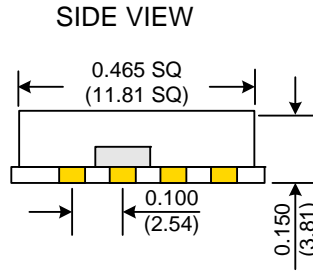
Phase Noise (1 Hz BW, Typical)



Tuning Curve (Typical)



Product Control:			
Crystek Part Number:	CVCO55CL-1693-2023	Release Date:	01-Mar-2016
Revision Level:	C	Responsible:	C. Vales



Pad	Connection
2	Vt
10	RF-OUTPUT
14	Vcc
Others	GROUND

- Unless otherwise specified, Dimensions are in: $\frac{IN}{(mm)}$
- Pad Location Dimensions are in: Inches

TAPE AND REEL



Drawing not to scale

Product Control:

Crystek Part Number:	CVCO55CL-1693-2023	Release Date:	01-Mar-2016
Revision Level:	C	Responsible:	C. Vales

Specification is subject to change without notice