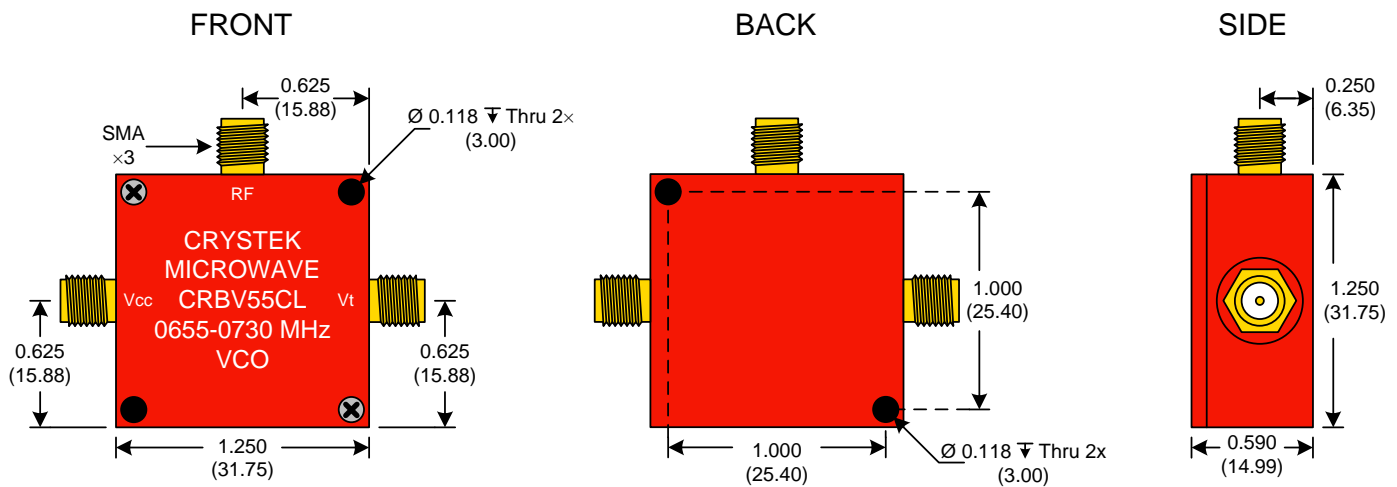




PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			655	MHz
Upper Frequency:	730			MHz
Tuning Voltage:	0.3		4.7	VDC
Supply Voltage:	4.75	5.0	5.25	VDC
Output Power:	0	+2.0	+4.0	dBm
Supply Current:			30	mA
Harmonic Suppression (2 <sup>nd</sup> Harmonic):	-8			dBc
Pushing:		0.5	1.0	MHz/V
Pulling, all Phases:		0.5	1.0	MHz pk-pk
Tuning Sensitivity:		25		MHz/V
Phase Noise @ 10kHz offset:		-110		dBc/Hz
Phase Noise @ 100kHz offset:		-132		dBc/Hz
Load Impedance:		50		$\Omega$
Input Capacitance:			220	pF
Operating Temperature Range:	-40		+85	$^{\circ}\text{C}$
Storage Temperature Range:	-45		+90	$^{\circ}\text{C}$



● Unless otherwise specified, Dimensions are in:  $\frac{\text{IN}}{\text{(mm)}}$

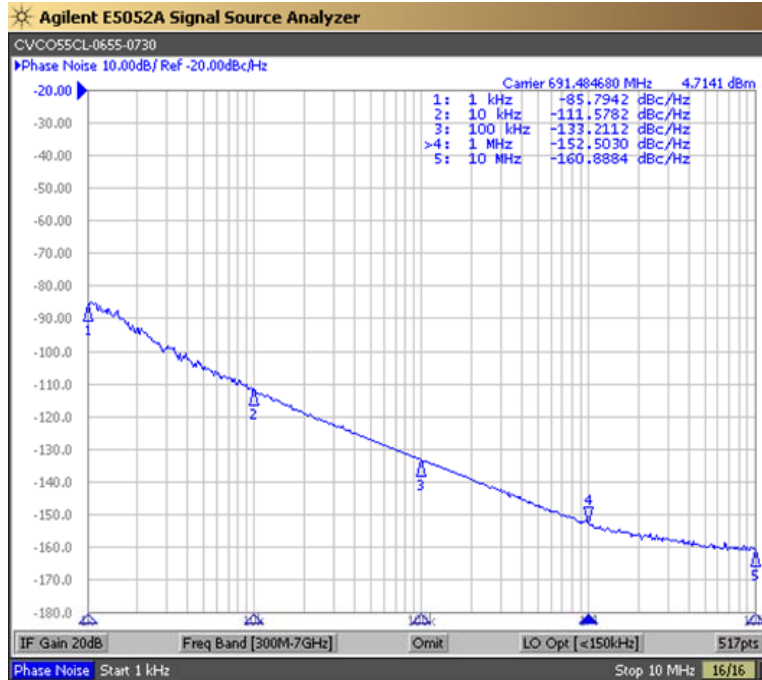
**Product Control:**

Crystek Part Number:	CRBV55CL-0655-0730	Release Date:	05-Nov-08
Revision Level:	A	Responsible:	C. Vales

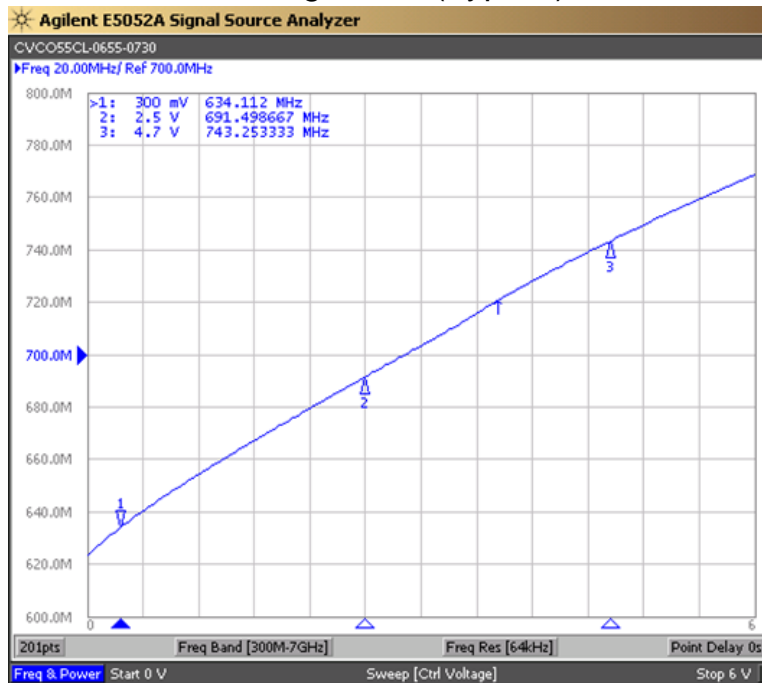




Phase Noise (1 Hz BW, Typical)



Tuning Curve (Typical)



**Product Control:**

Crystek Part Number:	CRBV55CL-0655-0730	Release Date:	05-Nov-08
Revision Level:	A	Responsible:	C. Vales

Specification is subject to change without notice

