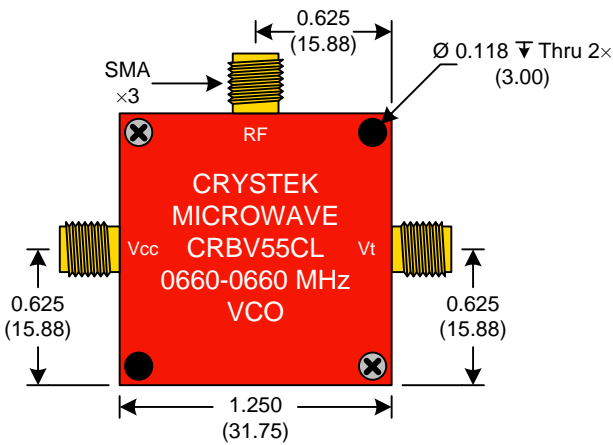




PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			660	MHz
Upper Frequency:	660			MHz
Tuning Voltage:	1.0		4.0	VDC
Supply Voltage:	7.6	8.0	8.4	VDC
Output Power:	+6.0	+9.0	+12.0	dBm
Supply Current:			25	mA
Harmonic Suppression (2 <sup>nd</sup> Harmonic)		-20		dBc
Pushing:			5.0	MHz/V
Pulling, all Phases:			15.0	MHz pk-pk
Tuning Sensitivity:		13		MHz/V
Phase Noise @ 10kHz offset:		-110		dBc/Hz
Phase Noise @ 100kHz offset:		-130		dBc/Hz
Load Impedance:		50		$\Omega$
Input Capacitance:			56	pF
Operating Temperature Range:	-40		+85	$^{\circ}\text{C}$
Storage Temperature Range:	-45		+90	$^{\circ}\text{C}$

FRONT



BACK



SIDE



● Unless otherwise specified, Dimensions are in:  $\frac{\text{IN}}{(\text{mm})}$

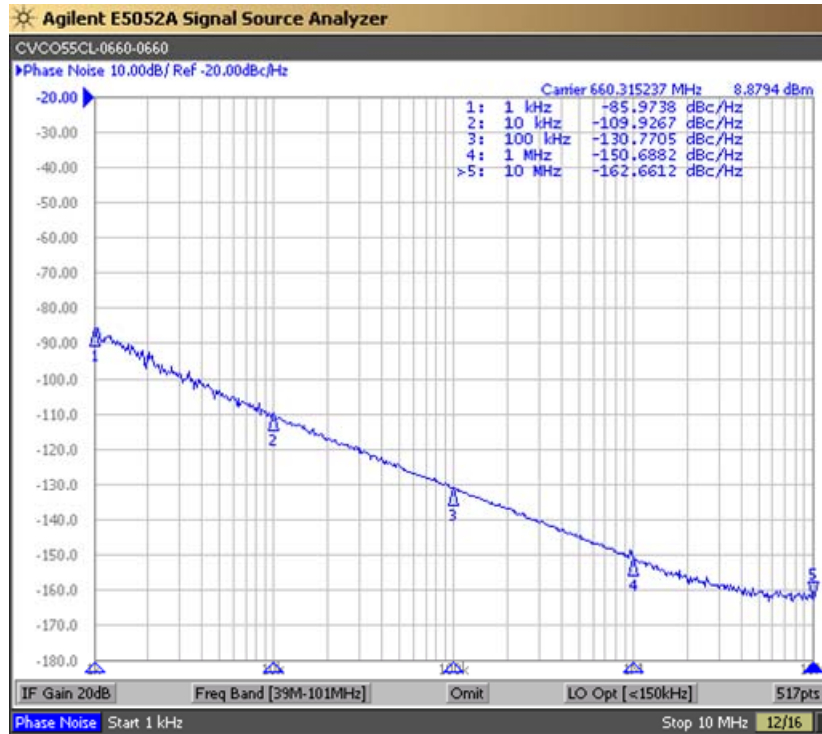
**Product Control:**

Crystek Part Number:	CRBV55CL-0660-0660	Release Date:	12-May-2015
Revision Level:	B	Responsible:	C. Vales

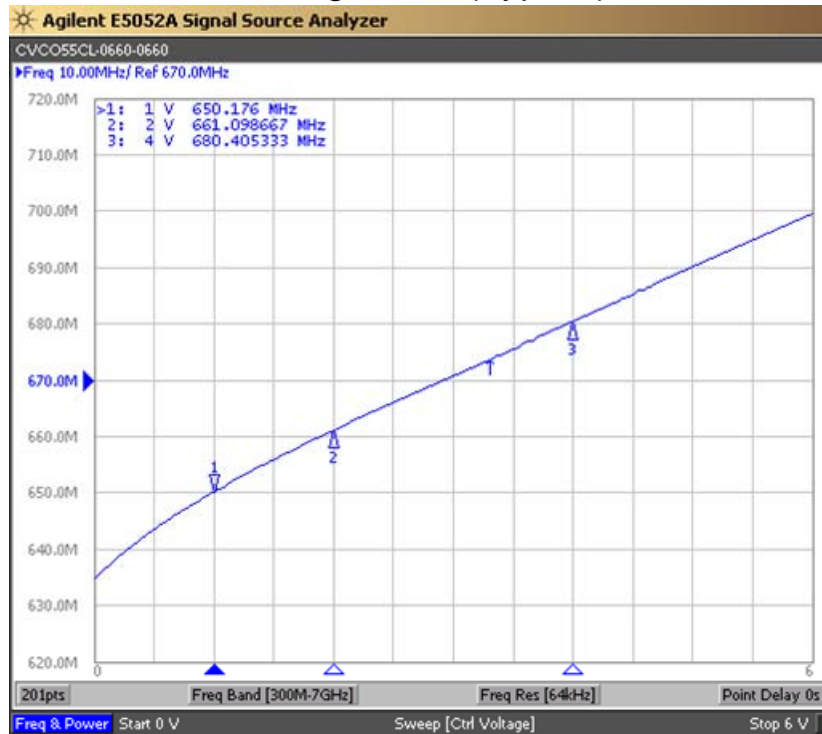




Phase Noise (1 Hz BW, Typical)



Tuning Curve (Typical)



**Product Control:**

Crystek Part Number:	CRBV55CL-0660-0660	Release Date:	12-May-2015
Revision Level:	B	Responsible:	C. Vales

Specification is subject to change without notice

