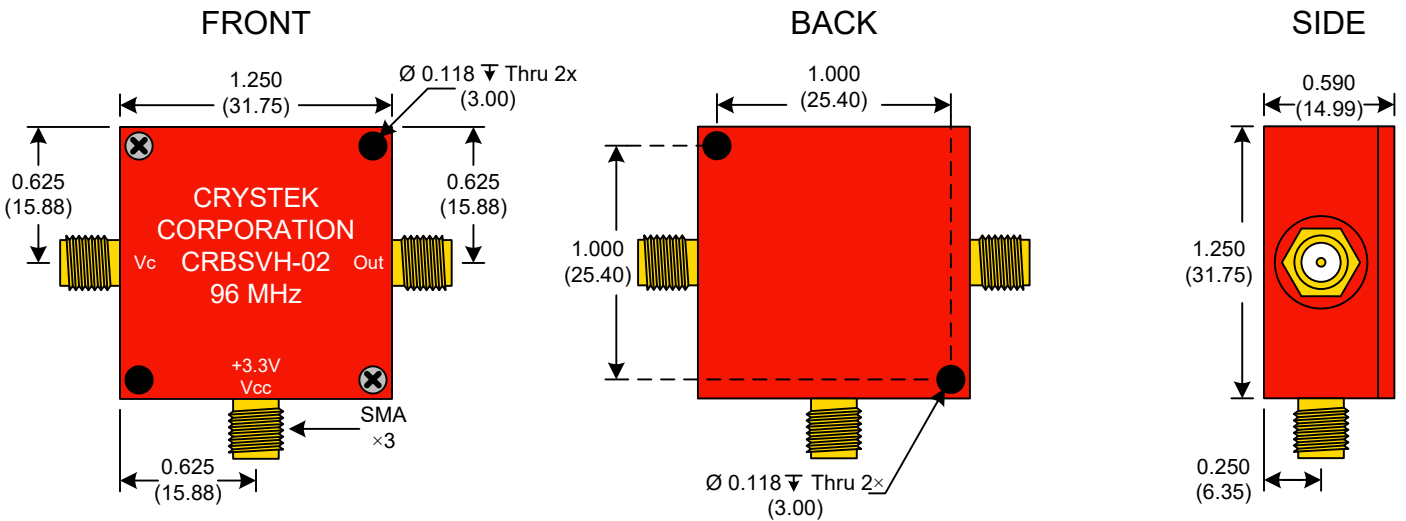




<b>Frequency:</b>	96.000 MHz
<b>Temperature Range:</b>	-40°C to 85°C
<b>Input Voltage:</b>	+3.3V ±5%
<b>Supply Pushing:</b>	1.2ppm/V Typical
<b>Input Current:</b>	15mA Typical, 25mA Max
<b>Output:</b>	CMOS
<b>Symmetry:</b>	45/55% Max @ 50%Vdd
<b>Rise/Fall Time:</b>	3nsec Max @ 20% to 80% Vdd
<b>Logic:</b>	"0" = 10% Vdd Max "1" = 90% Vdd Min
<b>Load:</b>	15pF
<b>Output Current:</b>	±24mA Max
<b>Input:</b>	
<b>Modulation Bandwidth:</b>	>10kHz @ -3dB
<b>Input Impedance:</b>	51 kΩ
<b>Control Voltage:</b>	1.65V ±1.65V
<b>Tuning Sensitivity:</b>	+25ppm/V Typical
<b>Frequency Pulling:</b>	±20ppm APR Min (Inclusive of frequency stability, calibration, and aging.)
<b>Linearity:</b>	±5% Max
<b>Phase Jitter (12kHz~20MHz):</b>	40 fsec Typical @100MHz
<b>Typical Phase Noise:</b>	
<b>1kHz</b>	-140 dBc/Hz
<b>10kHz</b>	-155 dBc/Hz
<b>100kHz</b>	-164 dBc/Hz
<b>1MHz</b>	-168 dBc/Hz
<b>Phase Noise Floor:</b>	-168 dBc/Hz Typical, -164 dBc/Hz Max
<b>Sub-harmonics:</b>	None
<b>Aging:</b>	<3ppm 1 <sup>st</sup> year, <1ppm thereafter



● Unless otherwise specified, Dimensions are in:  $\frac{IN}{(mm)}$

<b>Product Control:</b>			
Crystek Part Number:	CRBSVH-02-96.000	Release Date:	29-Mar-2017
Revision Level:	A	Responsible:	C. Vales