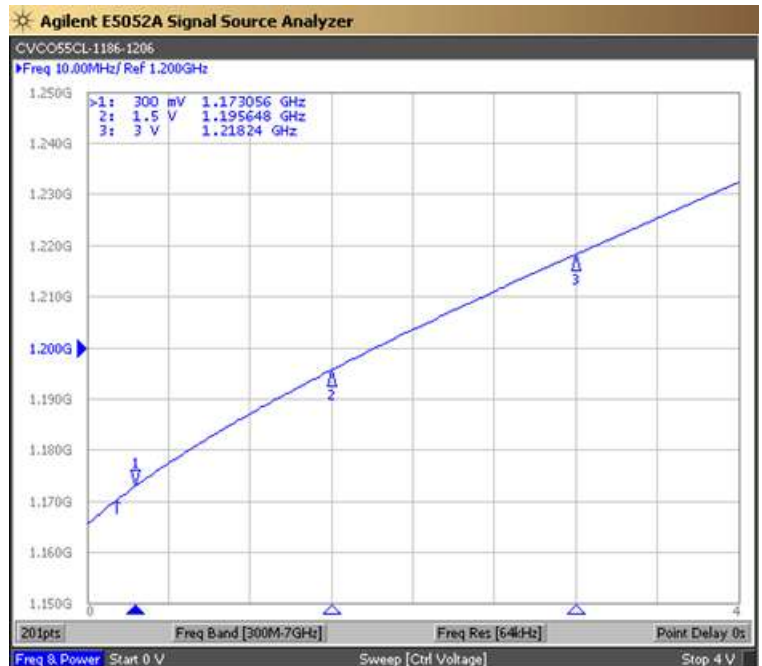
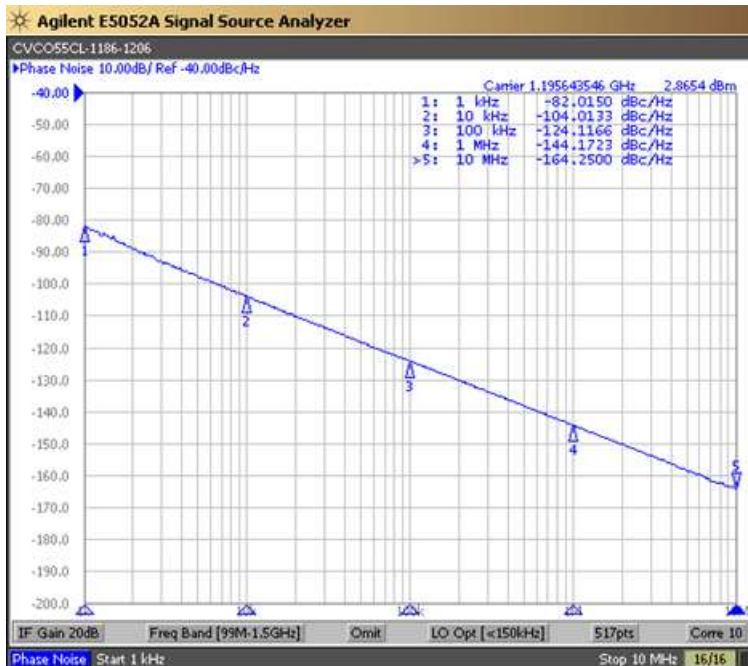


PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			1186	MHz
Upper Frequency:	1206			MHz
Tuning Voltage:	0.3		3.0	VDC
Supply Voltage:	2.97	3.3	3.63	VDC
Output Power:	0	+2.0	+4.0	dBm
Supply Current:		20		mA
Harmonic Suppression (2 <sup>nd</sup> Harmonic):		-15		dBc
Pushing:		0.5		MHz/V
Pulling, all Phases:		2.0		MHz pk-pk
Tuning Sensitivity:		15		MHz/V
Phase Noise @ 10kHz offset:		-103	-101	dBc/Hz
Phase Noise @ 100kHz offset:			-125	dBc/Hz
Load Impedance:		50		$\Omega$
Input Capacitance:			47	pF
Operating Temperature Range:	-40		+85	$^{\circ}\text{C}$
Storage Temperature Range:	-45		+90	$^{\circ}\text{C}$

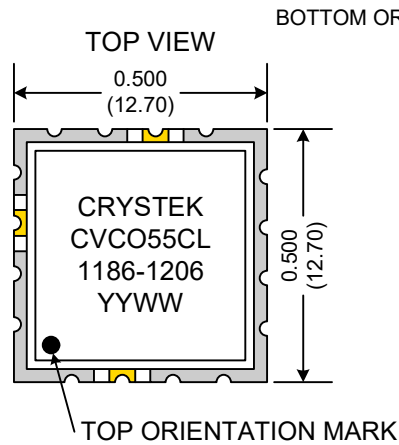
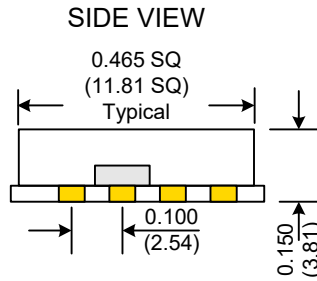
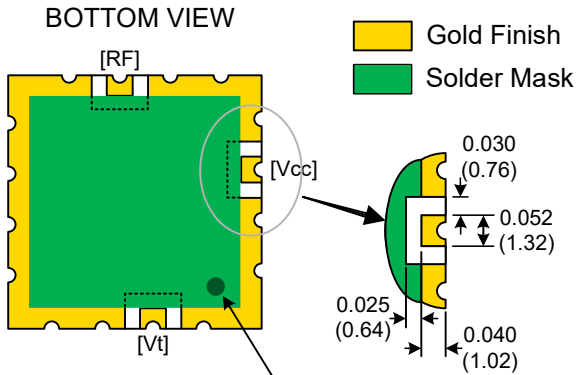
Phase Noise (1 Hz BW, Typical)

Tuning Curve (Typical)

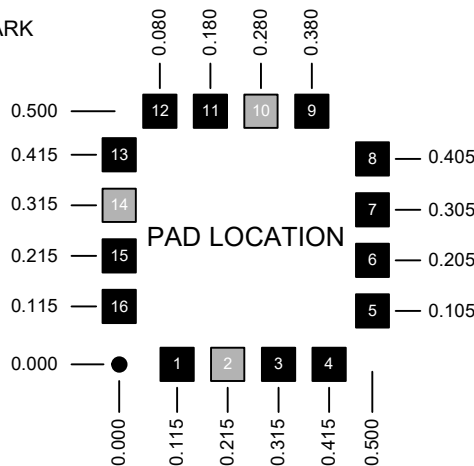


**Product Control:**

Crystek Part Number:	CVCO55CL-1186-1206	Release Date:	14-Jul-2025
Revision Level:	D	Responsible:	C. Vales



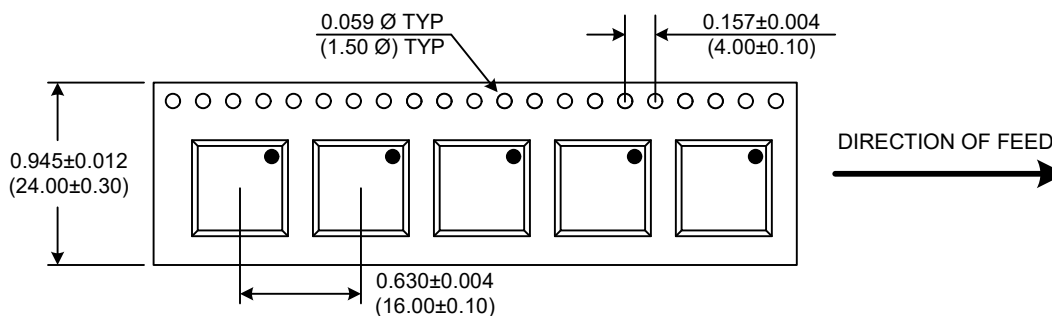
BOTTOM ORIENTATION MARK



Pad	Connection
2	Vt
10	RF-OUTPUT
14	Vcc
Others	GROUND

- Unless otherwise specified, Dimensions are in:  $\frac{IN}{(mm)}$
- Pad Location Dimensions are in: Inches

**TAPE AND REEL**



Drawing not to scale

Product Control:			
Crystek Part Number:	CVCO55CL-1186-1206	Release Date:	14-Jul-2025
Revision Level:	D	Responsible:	C. Vales

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