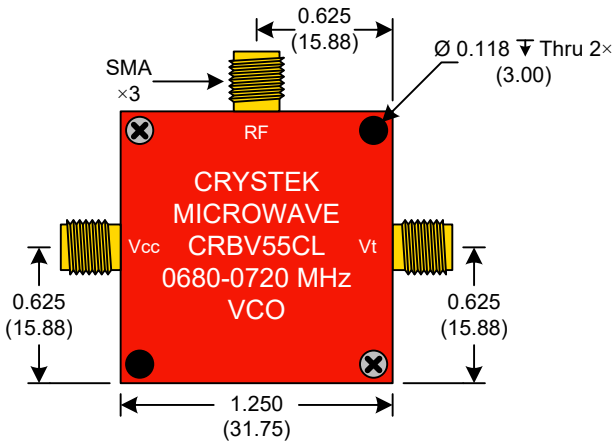


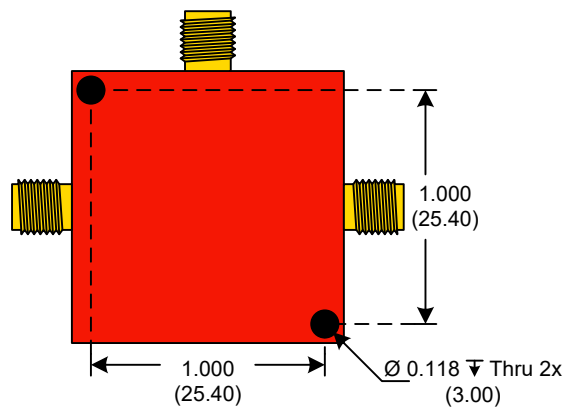


PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			680	MHz
Upper Frequency:	720			MHz
Tuning Voltage:	0.5		4.5	VDC
Supply Voltage:	4.75	5.0	5.25	VDC
Output Power:	0	+2.5	+5.0	dBm
Supply Current:			25	mA
Harmonic Suppression (2 nd Harmonic)		-15		dBc
Pushing:			1.0	MHz/V
Pulling, all Phases:			1.0	MHz pk-pk
Tuning Sensitivity:		29		MHz/V
Phase Noise @ 10kHz offset:		-109		dBc/Hz
Phase Noise @ 100kHz offset:		-129		dBc/Hz
Load Impedance:		50		Ω
Input Capacitance:			120	pF
Operating Temperature Range:	-40		+85	$^{\circ}\text{C}$
Storage Temperature Range:	-45		+90	$^{\circ}\text{C}$

FRONT



BACK



SIDE



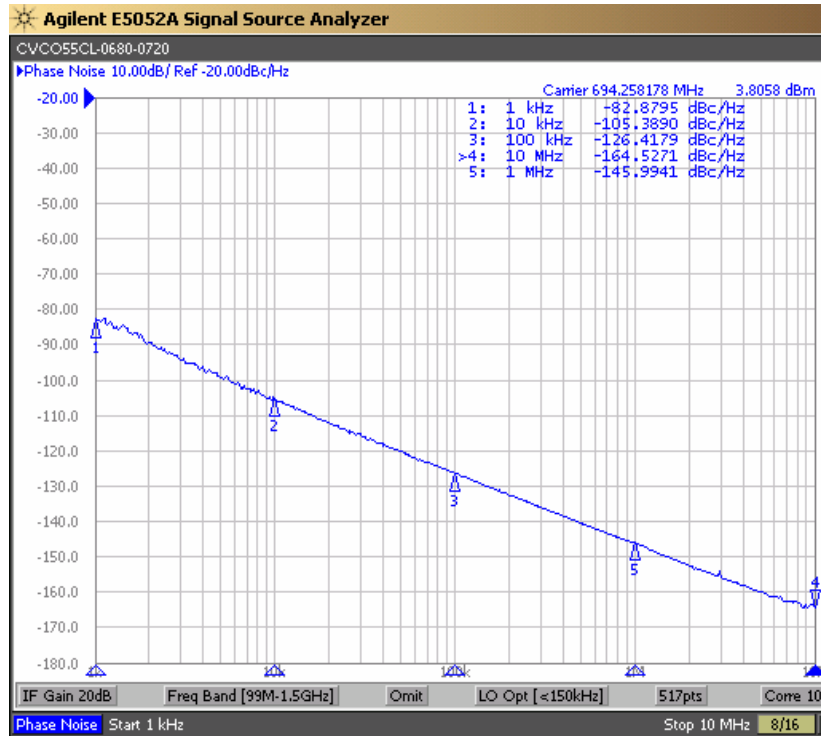
● Unless otherwise specified, Dimensions are in: $\frac{\text{IN}}{(\text{mm})}$

Product Control:

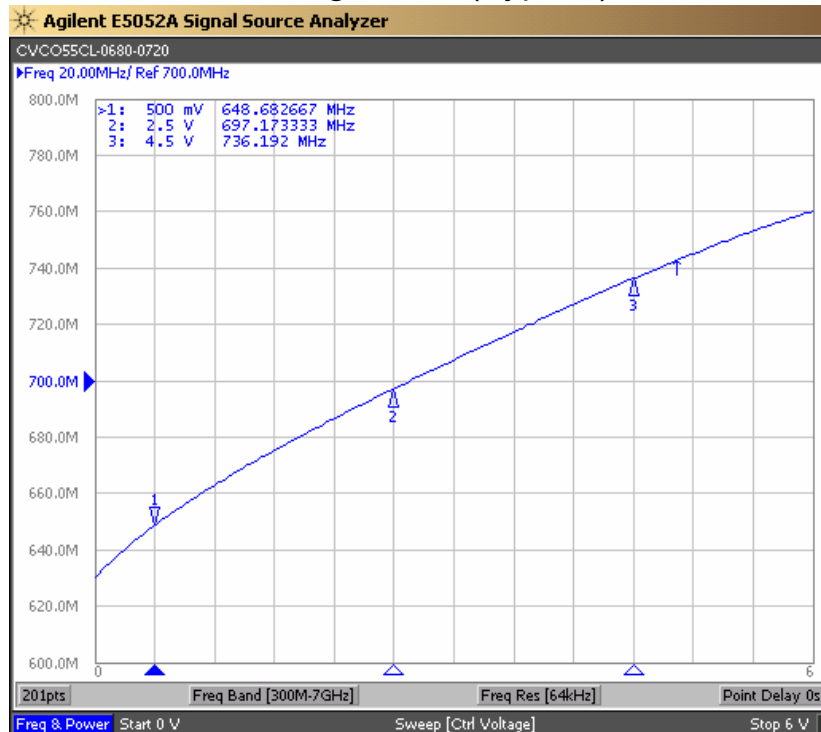
Crystek Part Number:	CRBV55CL-0680-0720	Release Date:	08-Feb-2018
Revision Level:	D	Responsible:	C. Vales



Phase Noise (1 Hz BW, Typical)



Tuning Curve (Typical)



Product Control:

Crystek Part Number:	CRBV55CL-0680-0720	Release Date:	08-Feb-2018
Revision Level:	D	Responsible:	C. Vales

Specification is subject to change without notice