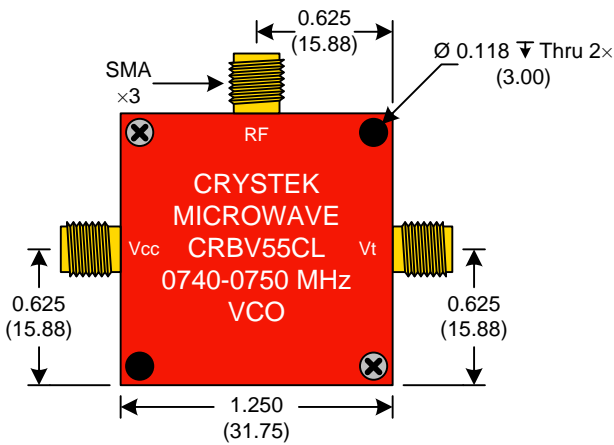




PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			740	MHz
Upper Frequency:	750			MHz
Tuning Voltage:	0.3		4.7	VDC
Supply Voltage:	4.75	5.0	5.25	VDC
Output Power:	-2.0	0	+2.0	dBm
Supply Current:		16	25	mA
Harmonic Suppression (2 nd Harmonic)		-15	-10	dBc
Pushing:		0.5	1.0	MHz/V
Pulling, all Phases:		0.5	1.0	MHz pk-pk
Tuning Sensitivity:		3		MHz/V
Phase Noise @ 10kHz offset:		-115		dBc/Hz
Phase Noise @ 100kHz offset:		-136		dBc/Hz
Load Impedance:		50		Ω
Input Capacitance:			150	pF
Operating Temperature Range:	-30		+70	$^{\circ}$ C
Storage Temperature Range:	-45		+90	$^{\circ}$ C

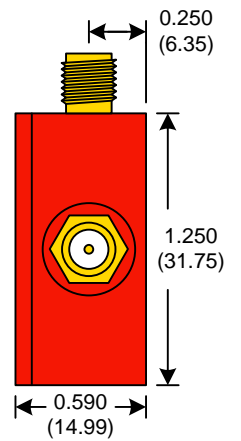
FRONT



BACK



SIDE



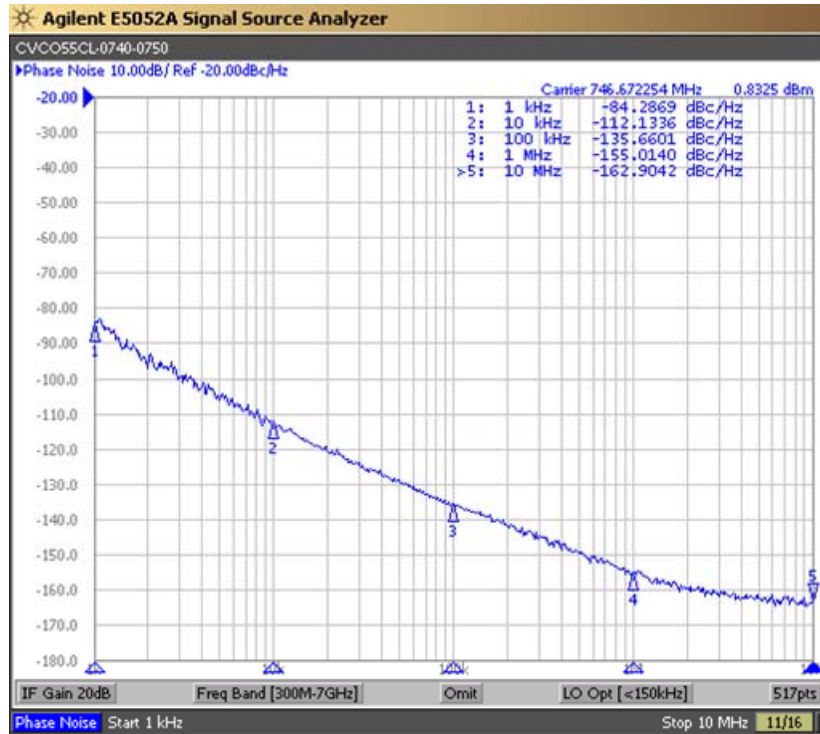
● Unless otherwise specified, Dimensions are in: $\frac{IN}{(mm)}$

Product Control:

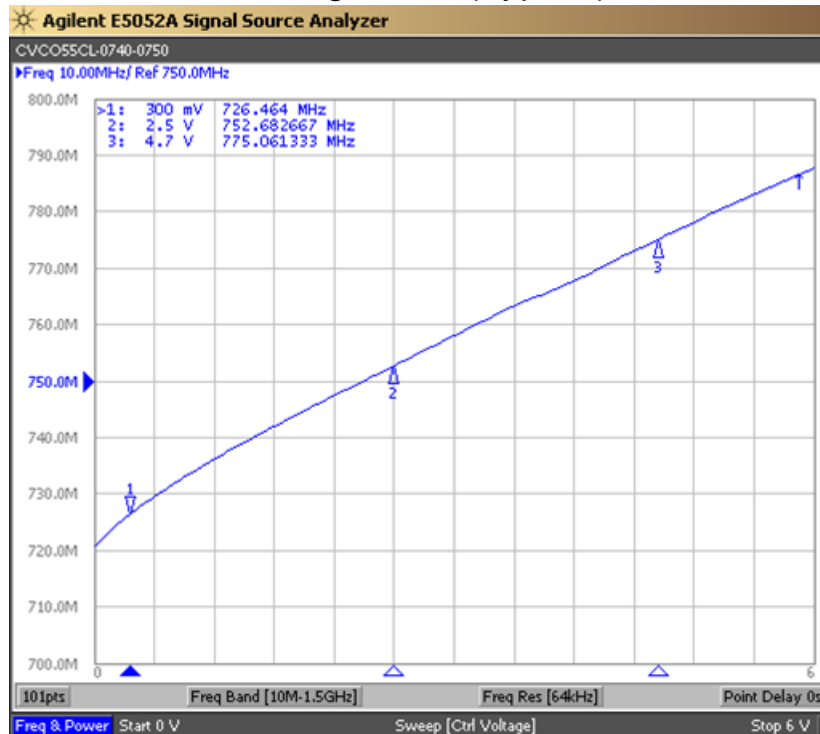
Crystek Part Number:	CRBV55CL-0740-0750	Release Date:	12-May-2015
Revision Level:	C	Responsible:	C. Vales



Phase Noise (1 Hz BW, Typical)



Tuning Curve (Typical)



Product Control:

Crystek Part Number:	CRBV55CL-0740-0750	Release Date:	12-May-2015
Revision Level:	C	Responsible:	C. Vales

Specification is subject to change without notice