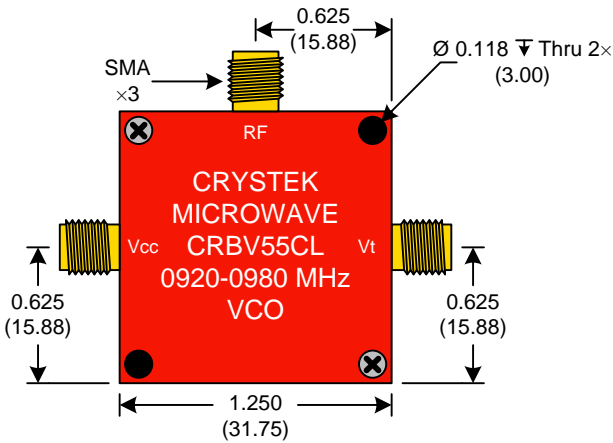




PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			920	MHz
Upper Frequency:	980			MHz
Tuning Voltage:	1.0		5.0	VDC
Supply Voltage:	4.75	5.0	5.25	VDC
Output Power:	+3.0	+6.0	+9.0	dBm
Supply Current:			35	mA
Harmonic Suppression (2 nd Harmonic)		-12		dBc
Pushing:		13.0		MHz/V
Pulling, all Phases:		3.0		MHz pk-pk
Tuning Sensitivity:		30		MHz/V
Phase Noise @ 10kHz offset:		-105	-100	dBc/Hz
Phase Noise @ 100kHz offset:		-128	-123	dBc/Hz
Load Impedance:		50		Ω
Input Capacitance:			30	pF
Operating Temperature Range:	-40		+85	°C
Storage Temperature Range:	-45		+90	°C

FRONT



BACK



SIDE



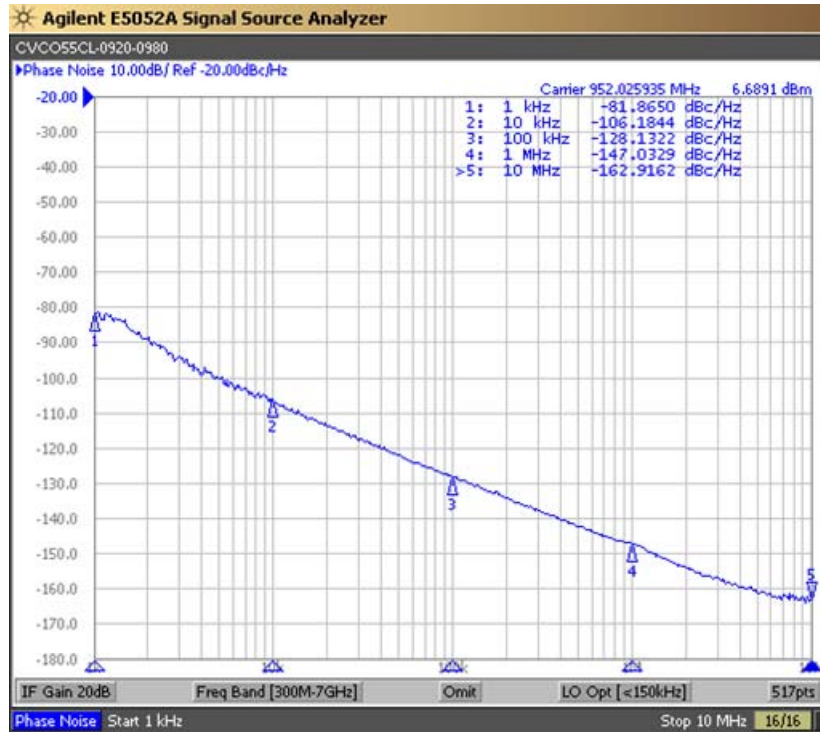
● Unless otherwise specified, Dimensions are in: $\frac{IN}{(mm)}$

Product Control:

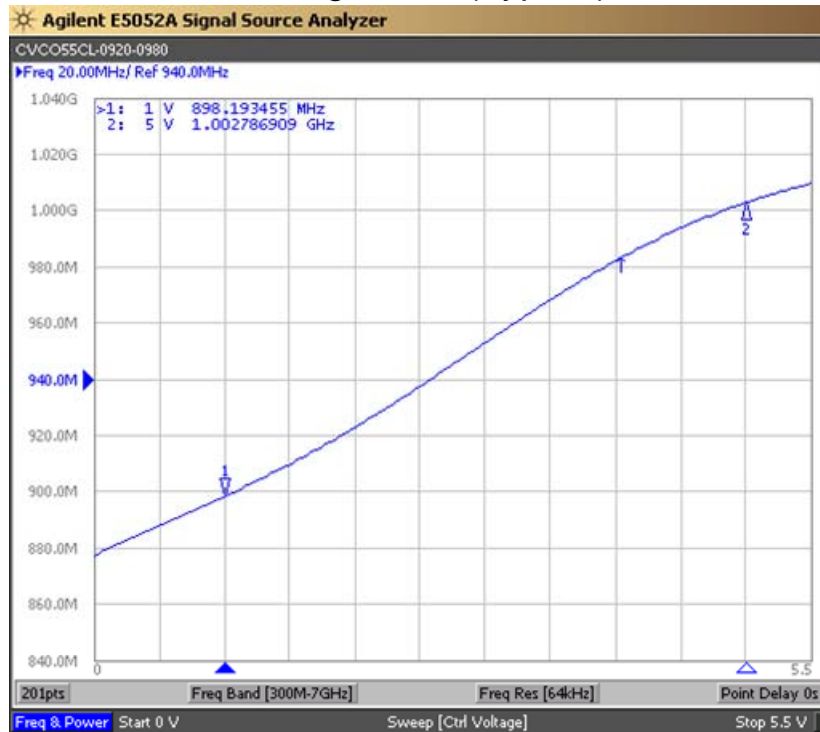
Crystek Part Number:	CRBV55CL-0920-0980	Release Date:	30-Sep-2015
Revision Level:	B	Responsible:	C. Vales



Phase Noise (1 Hz BW, Typical)



Tuning Curve (Typical)



Product Control:

Crystek Part Number:	CRBV55CL-0920-0980	Release Date:	30-Sep-2015
Revision Level:	B	Responsible:	C. Vales

Specification is subject to change without notice