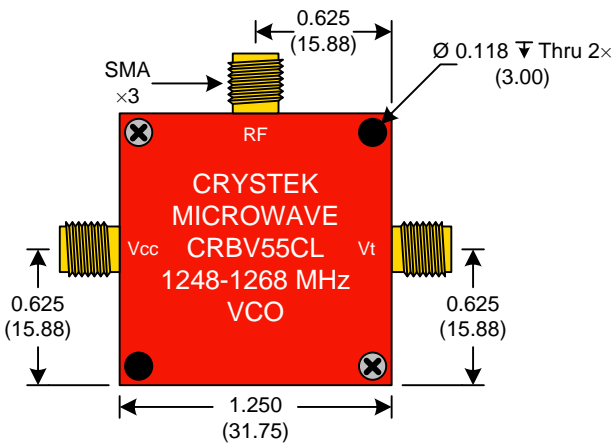




PERFORMANCE SPECIFICATION	MIN	TYP	MAX	UNITS
Lower Frequency:			1248	MHz
Upper Frequency:	1268			MHz
Tuning Voltage:	0.3		3.0	VDC
Supply Voltage:	2.97	3.3	3.63	VDC
Output Power:	+2.0	+4.0	+6.0	dBm
Supply Current:		20		mA
Harmonic Suppression (2 nd Harmonic)		-15		dBc
Pushing:		0.5		MHz/V
Pulling, all Phases:		2.0		MHz pk-pk
Tuning Sensitivity:		15		MHz/V
Phase Noise @ 10kHz offset:			-103	dBc/Hz
Phase Noise @ 100kHz offset:			-124	dBc/Hz
Load Impedance:		50		Ω
Input Capacitance:			47	pF
Operating Temperature Range:	-40		+85	$^{\circ}\text{C}$
Storage Temperature Range:	-45		+90	$^{\circ}\text{C}$

FRONT



BACK



SIDE



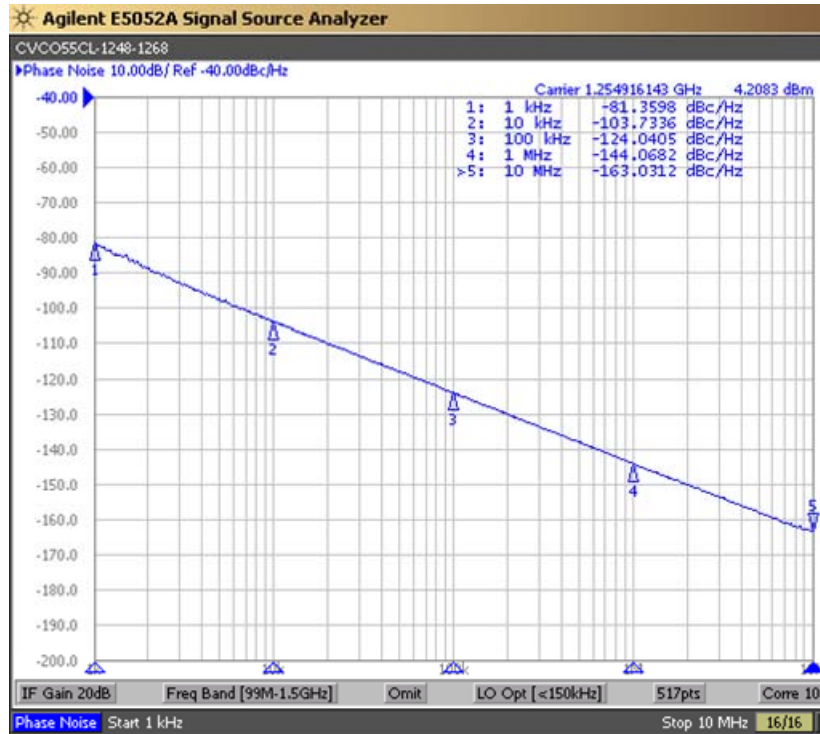
● Unless otherwise specified, Dimensions are in: $\frac{\text{IN}}{(\text{mm})}$

Product Control:

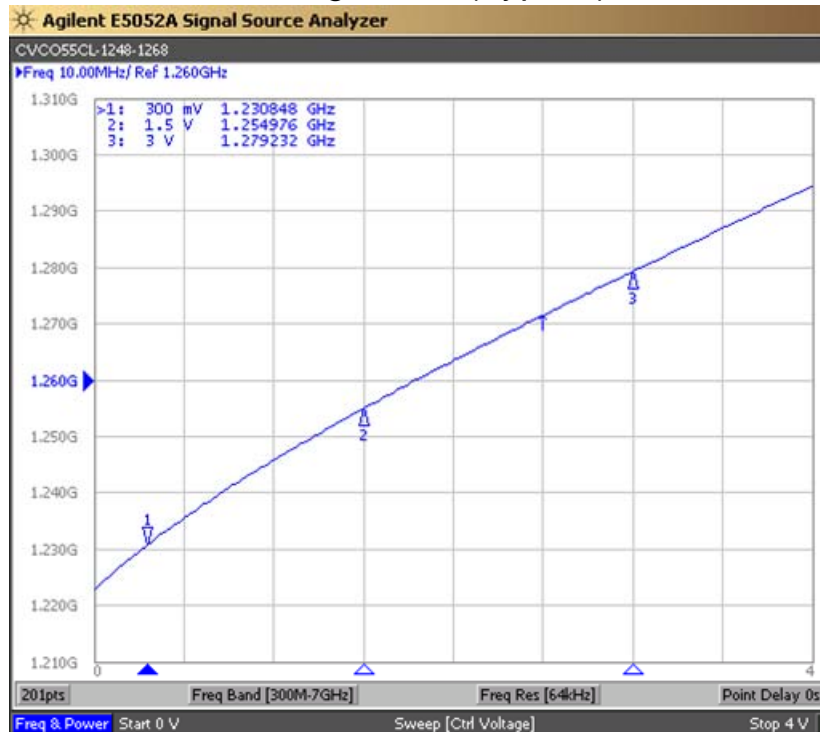
Crystek Part Number:	CRBV55CL-1248-1268	Release Date:	09-Dec-2015
Revision Level:	B	Responsible:	C. Vales



Phase Noise (1 Hz BW, Typical)



Tuning Curve (Typical)



Product Control:

Crystek Part Number:	CRBV55CL-1248-1268	Release Date:	09-Dec-2015
Revision Level:	B	Responsible:	C. Vales

Specification is subject to change without notice