



Frequency: 100.000 MHz
Temperature Range: -40°C to 85°C
Input Voltage: +5.5 V to +15 V
Input Current: 30 mA Max
Output: CMOS
 Symmetry: 45/55% Max @ 50%Vdd
 Rise/Fall Time: 3nsec Max @ 20% to 80% Vdd
 Logic: "0" = 10% Vdd Max
 "1" = 90% Vdd Min
 Load: 15pF
 Output Current: ±24mA Max

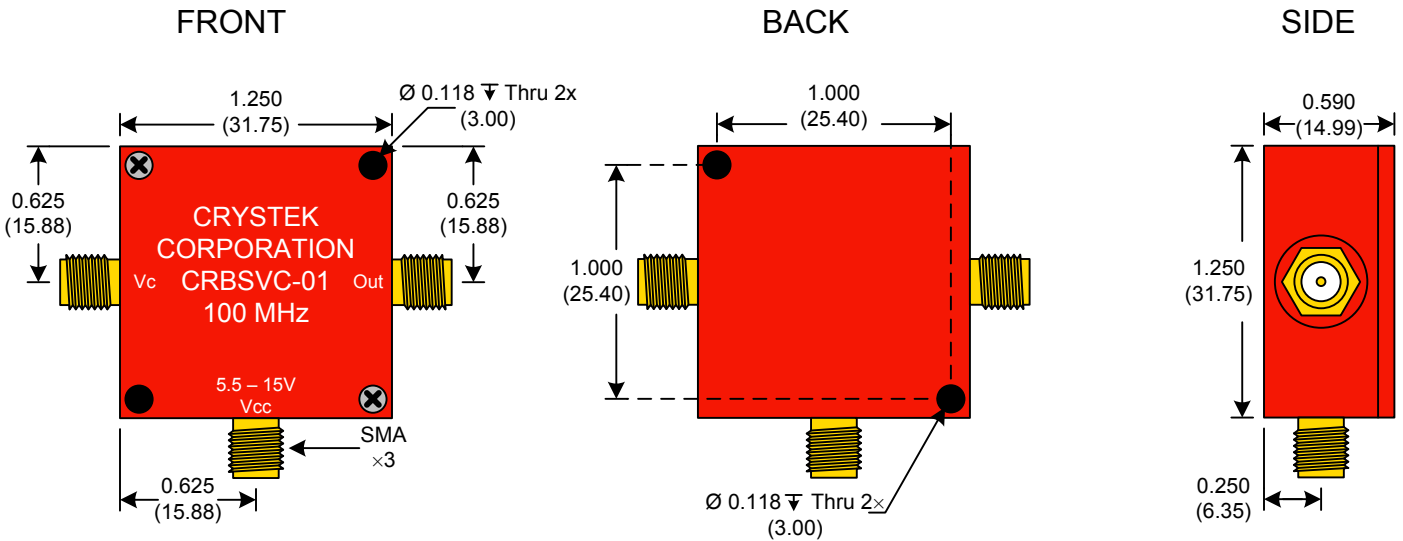
Input:
 Modulation Bandwidth: >10kHz @ -3dB
 Input Impedance: 51 kΩ
 Control Voltage: 1.65V ±1.65V
 Tuning Sensitivity: +25ppm/V Typical

Frequency Pulling: ±20ppm APR Min
 (Inclusive of frequency stability, calibration, and aging.)

Linearity: ±10% Max
Phase Jitter (12kHz~80MHz): 0.13psec Typical @100MHz
Phase Noise Floor: -165dBc/Hz Typical, -160dBc/Hz Max
Sub-harmonics: None
Aging: <3ppm 1st year, <1ppm every year thereafter

Typical Phase Noise:

1kHz	-135 dBc/Hz
10kHz	-155 dBc/Hz
100kHz	-164 dBc/Hz
1MHz	-165 dBc/Hz



● Unless otherwise specified, Dimensions are in: $\frac{IN}{(mm)}$

Product Control:			
Crystek Part Number:	CRBSVC-01-100.000	Release Date:	02-Jun-2015
Revision Level:	A	Responsible:	C. Vales