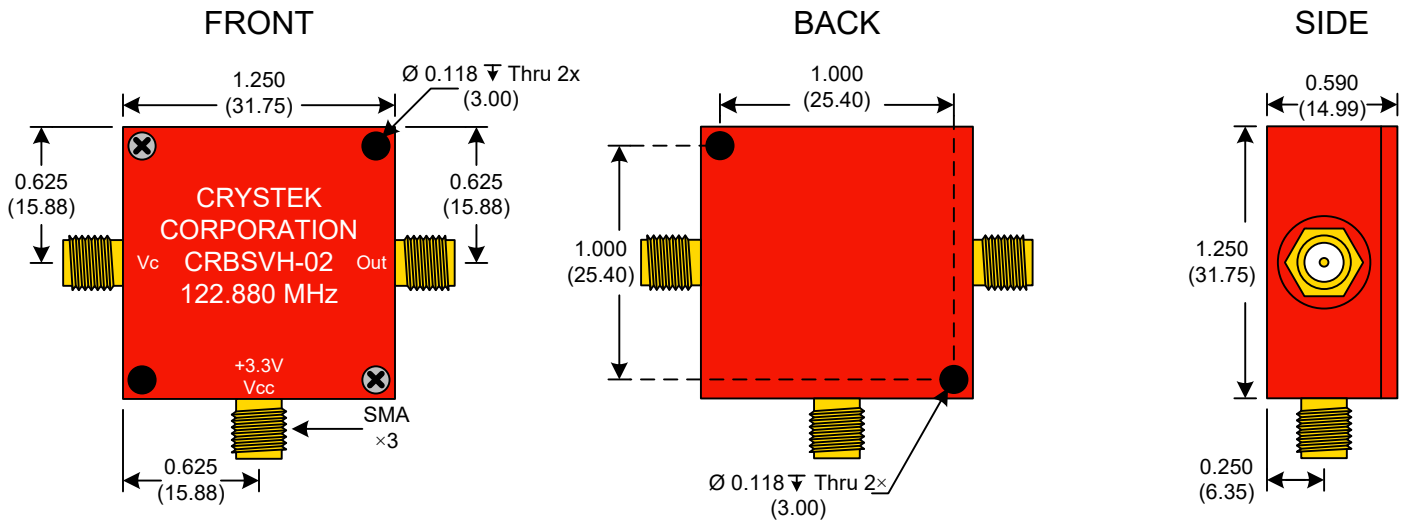




Frequency:	122.880 MHz
Temperature Range:	-40°C to 85°C
Input Voltage:	+3.3V ±5%
Supply Pushing:	1.2ppm/V Typical
Input Current:	15mA Typical, 25mA Max
Output:	CMOS
Symmetry:	45/55% Max @ 50%Vdd
Rise/Fall Time:	3ns Max @ 20% to 80% Vdd
Logic:	"0" = 10% Vdd Max "1" = 90% Vdd Min
Load:	15pF
Output Current:	±24mA Max
Input:	
Modulation Bandwidth:	>10kHz @ -3dB
Input Impedance:	51 kΩ
Control Voltage:	1.65V ±1.65V
Tuning Sensitivity:	+25ppm/V Typical
Frequency Pulling:	±20ppm APR Min (Inclusive of frequency stability, calibration, and aging.)
Linearity:	±5% Max
Phase Jitter (12kHz~20MHz):	40fs Typical
Typical Phase Noise:	
1kHz	-135 dBc/Hz
10kHz	-150 dBc/Hz
100kHz	-155 dBc/Hz
1MHz	-160 dBc/Hz
Phase Noise Floor:	-165 dBc/Hz Typical, -163 dBc/Hz Max
Sub-harmonics:	None
Aging:	<3ppm 1 st year, <1ppm thereafter



● Unless otherwise specified, Dimensions are in: $\frac{IN}{(mm)}$

Product Control:

Crystek Part Number:	CRBSVH-02-122.880	Release Date:	15-May-2019
Revision Level:	A	Responsible:	C. Vales

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