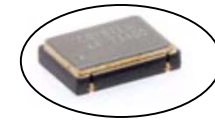


## C25xx Model

5x7 mm SMD, 2.5V, LVCMOS

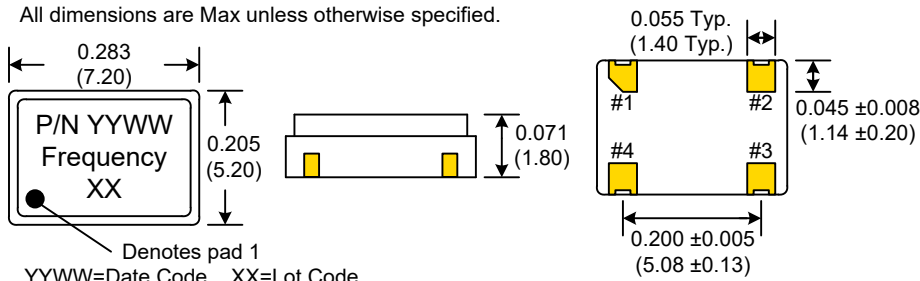
<b>Frequency Range:</b>	1.544 MHz to 70 MHz
<b>Frequency Stability:</b>	±20ppm to ±100ppm
<b>Temperature Range:</b>	
Operating:	-10°C to 70°C
(Option E)	-40°C to 85°C
<b>Storage Temperature:</b>	-45°C to 90°C
<b>Input Voltage:</b>	2.5V ± 0.125V
<b>Input Current:</b>	35mA Max
<b>Standby Current:</b>	10uA Max
<b>Output:</b>	LVCMOS
Symmetry:	45/55% Max @ 50% Vdd
Rise/Fall Time:	1ns Typical, 3.5ns Max
Logic:	"0" = 10% Vdd Max "1" = 90% Vdd Min
Load:	15pF Max
<b>Jitter RMS:</b>	12kHz~20MHz 0.5ps Typical, 1ps Max
<b>Aging:</b>	<3ppm 1 <sup>st</sup> year, <1ppm every year thereafter



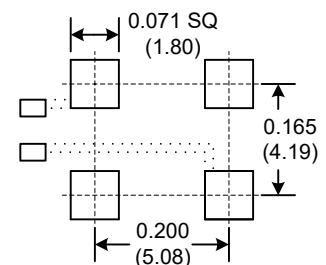
Designed to meet today's requirements for 2.5V applications. The C25xx Series utilizes only fundamental and 3<sup>rd</sup> overtone crystal designs. No multiplier is used thereby reducing output jitter. Available on 16mm tape and reel in quantities of 1K.

Dimensions inches (mm)

All dimensions are Max unless otherwise specified.

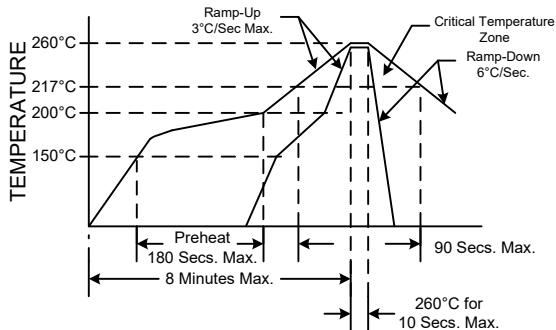


### SUGGESTED PAD LAYOUT



0.01uF Bypass Capacitor Recommended

### RECOMMENDED REFLOW SOLDERING PROFILE



NOTE: Reflow Profile with 240°C peak also acceptable.

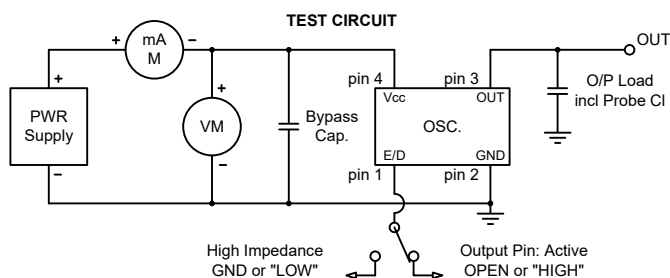
### Crystek Part Number Guide

Example: C2592-44.736MHz  
Example: CE2592-44.736MHz

Temperature		Frequency Stability
-10/70°C	-40/85°C	
C2590	CE2590	+/- 100ppm
C2592	CE2592	+/- 50ppm
C2591	CE2591	+/- 25ppm
C2598	NA	+/- 20ppm

### Enable/Disable

Function pin 1	Output pin
Open	Active
"1" level 0.7×Vcc Min	Active
"0" level 0.3×Vcc Max	High Z



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Rev: N

Date: 25-May-2023

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