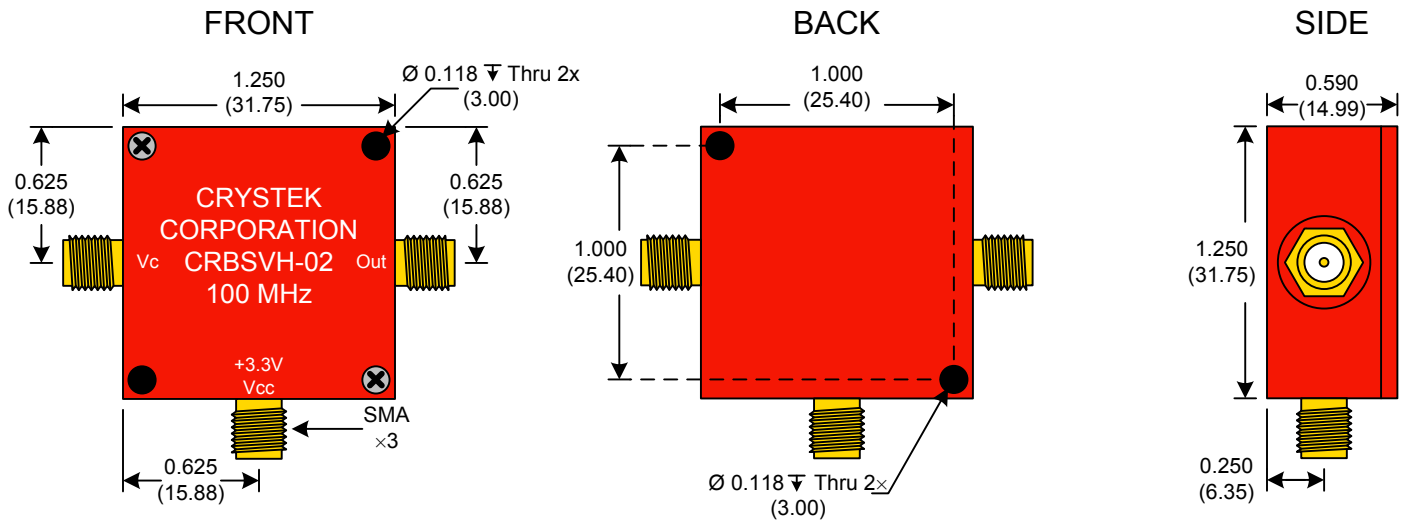




Frequency: 100.000 MHz
Temperature Range: -40°C to 85°C
Input Voltage: +3.3V ±5%
Supply Pushing: 1.2ppm/V Typical
Input Current: 15mA Typical, 25mA Max
Output: CMOS
Symmetry: 45/55% Max @ 50%Vdd
Rise/Fall Time: 3nsec Max @ 20% to 80% Vdd
Logic: "0" = 10% Vdd Max
 "1" = 90% Vdd Min
Load: 15pF
Output Current: ±24mA Max
Input:
Modulation Bandwidth: >10kHz @ -3dB
Input Impedance: 51 kΩ
Control Voltage: 1.65V ±1.65V
Tuning Sensitivity: +25ppm/V Typical
Frequency Pulling: ±20ppm APR Min
 (Inclusive of frequency stability, calibration, and aging.)
Linearity: ±5% Max
Phase Jitter (12kHz~20MHz): 40 fsec Typical @100MHz
Typical Phase Noise (100MHz):
 1kHz -140 dBc/Hz
 10kHz -155 dBc/Hz
 100kHz -164 dBc/Hz
 1MHz -168 dBc/Hz
Phase Noise Floor: -168 dBc/Hz Typical, -164 dBc/Hz Max
Sub-harmonics: None
Aging: <3ppm 1st year, <1ppm thereafter



● Unless otherwise specified, Dimensions are in: $\frac{IN}{(mm)}$

Product Control:			
Crystek Part Number:	CRBSVH-02-100.000	Release Date:	02-Dec-2015
Revision Level:	A	Responsible:	C. Vales